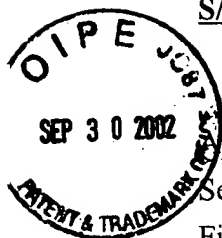


S/N 09/320,421



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Anh Quan Tran

Serial No.: 09/320,421

Group Art Unit: 2816

Filed: May 26, 1999

Docket: 303.586US1

Title: DRAM SENSE AMPLIFIER FOR LOW VOLTAGES

#2/Ext(1)
PATENT
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PETITION FOR A ONE-MONTH EXTENSION OF TIME

BOX RCE
Commissioner for Patents
Washington, D.C. 20231

In accordance with the provisions of 37 C.F.R. § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed May 22, 2002, said period of response being extended from August 22, 2002 to September 22, 2002 (Sunday).

Our check in the amount of \$110.00 is enclosed to cover the required extension fee.
Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6913

Date

9/23/2002

By

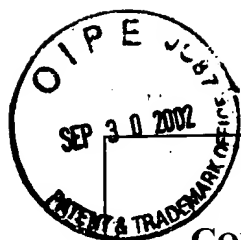
Edward J. Brooks, III
Reg. No. 40,925

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Box-RCE; Commissioner for Patents, Washington, D.C. 20231, on this 23rd day of September, 2002.

Name

Amy Moriarty

Signature



**REQUEST
FOR
CONTINUED EXAMINATION (RCE)
TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000,
provides for continued examination of an utility or plant application
filed on or after June 8, 1995.
See The American Inventors Protection Act of 1999 (AIPA).


Application Number	09/320,421
Filing Date	May 26, 1999
First Named Inventor	Leonard Forbes et al.
Group Art Unit	2816
Examiner Name	Anh-Quan Tra
Attorney Docket Number	303.586US1

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application entitled DRAM SENSE AMPLIFIER FOR LOW VOLTAGES.

Submission required under 37 C.F.R. § 1.114

1. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on ☐
2. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on ☐
3. ☒ An Amendment Under 37 CFR § 1.116 (12 pages) is enclosed.
4. ☐ A new power of attorney (☐ pages) is enclosed.
5. ☐ An Information Disclosure Statement is enclosed (☐ pages)
 - a. ☐ Form(s) 1449
 - b. ☐ Copies of IDS Citations
6. ☒ A check in the amount of \$740.00 is attached to pay the RCE filing fee required under C.F.R. § 1.17(e).
7. ☒ The Commissioner is hereby authorized to credit overpayments or charge any fees set forth in 37 C.F.R. §§ 1.16 through 1.18 to Deposit Account No. 19-0743.
8. ☒ A petition for extension of time in the prior application (1 pg) is enclosed along with a check in the amount of \$110.00 to pay the extension fee.
9. ☒ Other: Clean Version of Pending Claims (10 pages)

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By 
Atty: Edward J. Brooks, III
Reg. No. 40,925

Customer Number **21186**

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Box RCE, Washington, D.C. 20231, on this 23rd day of September, 2002.

Name

Amy Moriarty

Signature

Amy Moriarty

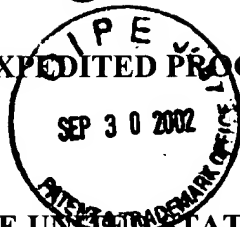
10/01/2002 CNGUYEN 00000147 09320421

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740.00 OP

EXPEDITED PROCEDURE - EXAMINING GROUP 2816

S/N 09/320,421



23/D
10/10/02
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TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Anh-Quan Tra

Serial No.: 09/320,421

Group Art Unit: 2816

Filed: May 26, 1999

Docket: 303.586US1

Title: DRAM SENSE AMPLIFIER FOR LOW VOLTAGES

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

Box RCE
Commissioner for Patents
Washington, D.C. 20231

In response to the final Office Action mailed May 22, 2002, please amend the application as follows:

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from 22 August 2002 to 22 September 2002.

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 10, 17, 23, 29, 32, 33, 37, 40, 44, and 45. The specific amendments to individual claims are detailed in the following marked up set of claims.

10. (Four Times Amended) A latch circuit, comprising:

a pair of cross-coupled amplifiers, wherein each amplifier includes:

a first transistor of a first conductivity type;

a dual-gated metal-oxide semiconducting field effect transistor (MOSFET) of a

second conductivity type, wherein a drain region for the dual-gated

MOSFET is coupled to a drain region of the first transistor in the same

amplifier, is coupled directly to a gate of the first transistor of the first

conductivity type in the other amplifier in the pair of cross-coupled